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Peter Yim

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DATE:

September 13, 2006

Number of pages	13	
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U.S. Patent Application Serial No. 10/802,549

For: ADJUSTING DIE PLACEMENT ON A SEMICONDUCTOR

WAFER TO INCREASE YIELD

By: Eitan CADOURI

Our Reference: 52432-20006.00

Attached is the following:

- 1. Transmittal (1 page)
- 2. Fee Transmittal (in duplicate, 2 pages)
- 3. Amendment Under 37 C.F.R. 1.111 (9 pages)

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TRANSMITTAL FORM (to be used for all correspondence after initial filing)		Filing Date		March 16, 2004		
		First Named Inventor		Eitan CADOURI		
				2818		
			Examiner Name		P. Dang	
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rinted name	Peter J. Yim					
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Non-English Specification, \$130 fee (no small entity discount)									
Other (e.g., late filing surcharge):									
SUBMITTED BY	102 1 × /		Registration No.	44.44	1	(448) 666	6070		
Signature	1.5.V		(Attorney/Agent)	44,417	Telephone	(415) 268			
Name (Print/Type)	Peter J. Yim				Data S	eptember	13, 2006		

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Docket No.: 524322000600

(PATENT)

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Dated: September 13, 2006

Signature: Valene Cohen

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Eitan CADOURI

Application No.: 10/802,549

Confirmation No.: 3719

Filed: March 16, 2004

Art Unit: 2818

For: ADJUSTING DIE PLACEMENT ON A

SEMICONDUCTOR WAFER TO INCREASE

YIELD

Examiner: P. Dang

AMENDMENT UNDER 37 C.F.R. 1.111

MS Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

This is in response to the non-final Office Action dated June 14, 2006 (Paper No. Part of Paper No./Mail Date 061206), for which a response is due on September 14, 2006. Accordingly, this response is timely filed. Reconsideration and allowance of the pending claims, as amended, in light of the remarks presented herein are respectfully requested.

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